

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

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AssignedAPPLICANT(S): Hisashi NAGATA,
et al.

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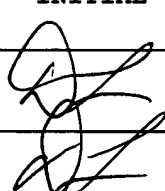
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ART UNIT:

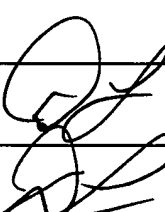
Unassigned

1017 U.S. PTO
10/024065
12/18/01


UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FIL. DATE IF APPR
	AA	4,694,287	September 15, 1987	A. Chenevas- Paule, et al.			
	AB	09/803,509	Pending Application	Hisashi Nagata, et al.			March 9, 2001

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRAN YES/NO
	BA	08-234237	September 13, 1996	JAPAN			Abstract
	BB	11-326934	November 26, 1999	JAPAN			Abstract

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	CA	Higuchi, et al., "Electronic Circuits for Information Systems", Vol. II, pp. 44-47, 1989.
	CB	Tanaka, et al., "An LCD Addressed by a Si:H TFTs with Peripheral poly-Si TFT Circuits", IEDM93, pp 389-392, 1993 IEEE.
	CC	Kato, et al., "Ar+Laser Annealed Poly-Si TFTs for Large Area LCDs" Euro Display '96, pp. 551-554, 1996.

Examiner:

Date:

David L Lewis

12/9/03